

DESCRIPTION:

This document describes Aplus 4GBx 64-bit 32GB DDR5 SDRAM (Synchronous DRAM) Dual In-Line Memory Module. The components on this module include sixteen 2GB x 8-bit DDR5 SDRAMs in FBGA packages and a 4M-bit serial EEPROM. Those components were mounted on a 262-pin printed circuit board. This 262-pin SO_DIMM is used to be mounted into 262-pin edge connector sockets and data I/O transactions could be apply on both edges of DQS. The electrical and mechanical specifications are as follows:

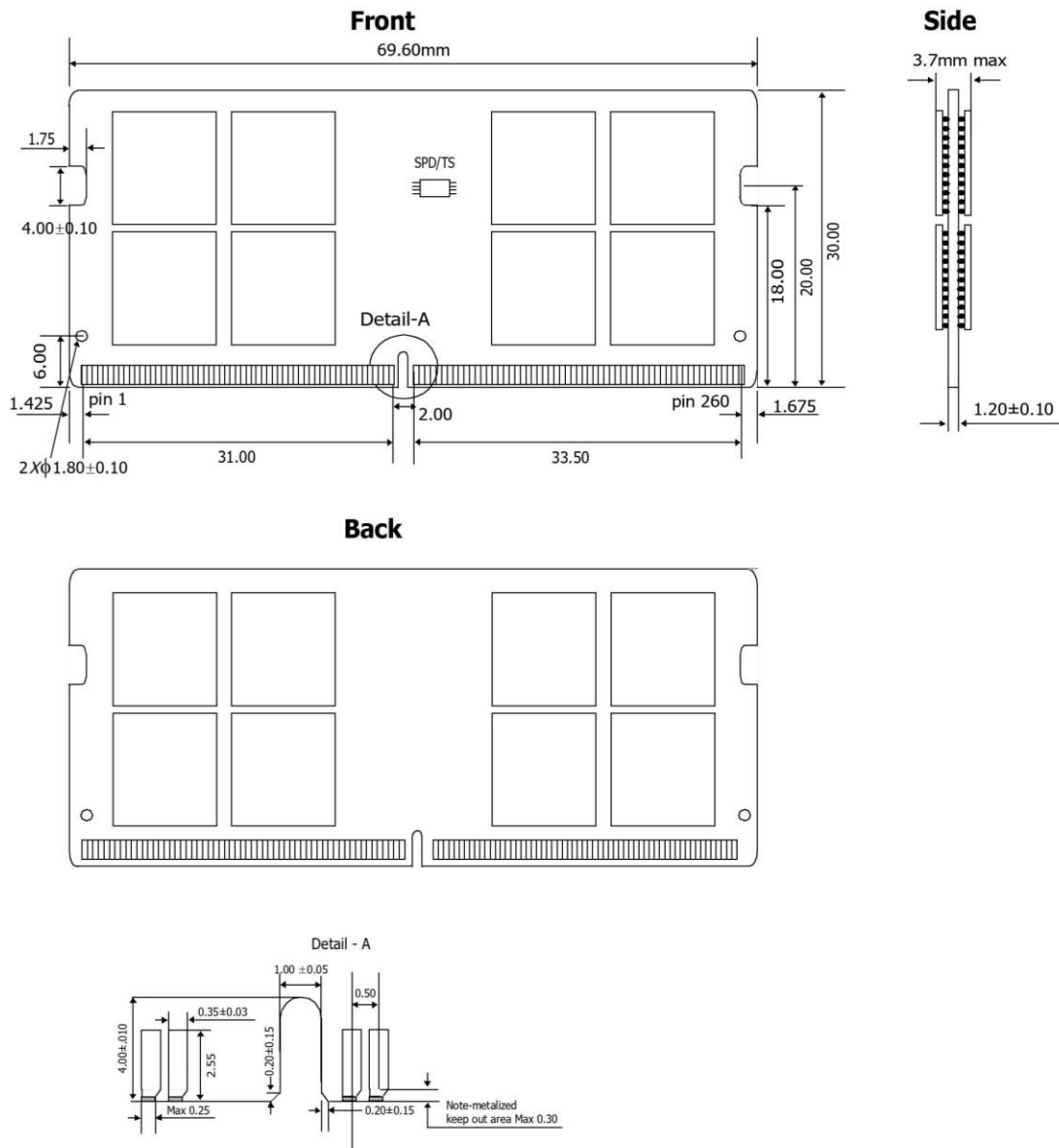
FEATURES:

- DRAM VDD/VDDQ = 1.1V (-33mV / +66mV)
- DRAM VPP = 2.5V (-125mV / +250mV)
- Memory Standard : DDR5-4800/PC5-38400
- BL16, BC8 OTF, BL32, BL32 OTF supported
- Same Bank Refresh
- VrefDQ / VrefCA / VrefCS Training
- Hard/Soft Post Package Repair
- Input Clock Frequency Change
- Maximum Power Saving Mode (MPSM)
- Multi-Purpose Command (MPC)
- Per DRAM Addressability (PDA)
- Read Training Mode
- CA Training Mode
- CS Training Mode
- Per Pin VREFDQ Training
- Write Leveling Training Mode
- Connectivity Test (CT)
- ZQ Calibration
- DFE (Decision Feedback Equalization) for DQ
- DQS Interval Oscillator
- 1N / 2N Mode support for Commands
- On-Die ECC
- ECC Transparency and Error Scrub
- CRC (Cyclic Redundancy Check)
- Loopback for multiple purposes - monitor data, BER(Bit Error Rate) analysis, etc.
- Module: 262-pin SO-DIMM
- Memory Rank : Dual-rank
- Operating temperature : 0°C - 85°C

PERFORMANCE:

Industry	Data Rate (MT/s)	^t CK (ns)	CAS Latency (^t CK)	^t RCD (ns)	^t RP(ns)	^t RAS (ns)	^t RC (ns)	Clock Cycles (CL- ^t RCD- ^t RP)
Nomenclature	CL=40							
PC5-4800	4800	0.416	16.00	16.00	16.00	32.00	48.00	40-39-39

262-Pin DDR5 SODIMM



Note:
1. ±0.13 tolerance on all dimensions unless otherwise stated.

Units: millimeters